## **LOGIC ANALYZERS**

Portable Logic Analyzers HP 1652B, 1653B





- · 80 channels of state/timing
- · 2 channels of 400 MSa/s digitizing oscilloscope
- More measurement power at a lower cost than separate instruments

Logic Analyzers with a Digitizing Oscilloscope

The HP 1652B and HP 1653B Logic Analyzers have all of the features of the HP 1650B and HP 1651B plus two 400 MSa/s digitizing oscilloscope channels, automatic pulse parameter measurements, and time-correlated state, timing, and oscilloscope displays. You still can completely analyze your 8-, 16-, or 32-bit microprocessor while getting better definition on system signals with the 2-channel oscilloscope.

You can characterize critical timing parameters with time interval measurements to better than 1 ns accuracy or examine glitches in your system with the built-in scope to determine if noise or loading is the problem. Or, you can use the scope to enhance your troubleshooting capabilities.

Two Simultaneous 400 MSa/s Analog Channels

Each scope channel is a full-featured, 400 MSa/s, 100 MHz bandwidth oscilloscope. Both channels simultaneously capture nonrepeating events with a full 2,048 samples per channel. The built-in scope is based on the same technology used in the popular HP 54502A 100 MHz Single-Shot BW Oscilloscope. The scope features include precision voltage and time-interval measurements, autoscale, waveform math, auto-calibration, infinite persistence, and averaging display modes.

## Time-Correlated State, Timing, and Oscilloscope Measurements

System debugging becomes easier when you display time-correlated state, timing, and analog displays on the same screen. You can see how hardware and software interact, while getting an accurate view of how your system sees the signal.

Cross-Trigger Measurement Modules

You can use the state analyzer's powerful triggering capabilities to determine when the oscilloscope should trigger. Glitch triggering on all channels makes the timing analyzer another great tool for triggering the scope. Simply set up the timing analyzer to trigger on a glitch, then trigger the oscilloscope to capture the activity around the glitch. By getting an analog display of the signal, you can determine whether the glitch is really a problem.

Label >	ADDR	68000 Mnemonic	STAT
Base >	Hex	hex	Symbo1
-0004	00300E	SUBQ.L #1,D0	SUPR PGRM READ
-0003	003010	BNE.B 00300E	SUPR PGRM READ
-0002	003012	MOVE.B *******,*******	SUPR PGRM READ
-0001	00300E	SUBQ.L #1,D0	SUPR PGRM READ
+0000	003010	BNE .B 00300E	SUPR PGRM READ
+0001	003012	MOVE .B *******	SUPR PGRM READ
+0002	00300E	SUBQ.L #1,D0	SUPR PGRM READ
ime/Div	1.000 m	s Delay 3.600 ms 0 to	Trigger 7.100 ms
1000		× -	0
H 2			

**Portable Analyzers** 

The HP 1652B/1653B Portable Analyzers are ideal for service applications. Their small size and light weight (just 24 lb) make them easy to carry to test sites. With the built-in scope, you have two complete instruments in one small package.

- See analog events with a general-purpose 100 MHz single-shot BW digitizing scope for viewing analog events
- Automatic pulse parameter measurements



See Pre-Trigger Events

2 K sample memory per oscilloscope channel lets you view events up to 5 s before the trigger, while maintaining better than 1-ns time-interval accuracy.

## **Automatic Pulse Parameter Measurments**

Quickly analyze a signal's analog properties without having to count graticules. Choose automatic measurements or time markers to measure voltage and timing relationships. The HP 1652B/1653B automatically measures the following pulse parameters:

+ pulse width
frequency
rise time
peak-to-peak voltage
preshoot

- pulse width
period
fall time
overshoot

## **Automatic Marker Search**

Using the automatic marker search, you can examine waveforms for specific patterns that could be the cause of a system crash. Or use the automatic marker search statistics to reveal setup and hold-time violations as you make repeated measurments on the system. After each run, the markers are placed on specified patterns, and statistics are compiled on the mean, minimum, and maximum marker placement times, so you can see how often a specific event occurs.

**Hardcopy Output** 

After using the built-in oscilloscope to find an elusive problem, use either an HP-IB or RS-232 printer to obtain a permanent record. The HP 1652B and 1653B support over 10 printers.

## All Other Features of the HP 1650B/1651B

All of the other features of the HP 1650B/1651B logic analyzers are included in the HP 1652B/1653B. These features include 80/32 channels of state and timing analysis, full-featured triggering, built-in disk drives, and support for most popular processors and bus interfaces. And the data and configuration files of the HP 1652B/1653B are compatible with the HP 1650B/1651B/1654B and with the HP 16510B. You can transfer information from one analyzer to another.

### **Key Specifications and Characteristics**

	HP 1652B	HP 1653B	
Timing	100 MHz all 80 channels	100 MHz all 32 channels	
State	35 MHz all 80 channels	35 MHz all 32 channels	
Analog	2 – 400 MSa/s 100 MHz BW Simultaneous acquisition channels	2 – 400 MSa/s 100 MHz BW Simultaneous acquisition channels	
Glitch capture	80 channels	32 channels	
Microprocessor support	Most 8-, 16-, and 32-bit microprocessors, buses	Most 8-bit microprocessors, buses	

See page 343 for ordering information.

# LOGIC ANALYZERS 100

Portable Logic Analyzers HP 1650B, 1651B, 1654B

327

- HP 1650B: 80 channels of 35 MHz state/100 MHz timing
- HP 1651B: 32 channels of 35 MHz state/100 MHz timing
- HP 1654B: 64 channels of 35 MHz state/100 MHz timing
- Broad support for microprocessors, buses and interfaces
- · Simultaneous state/state or state/timing measurements





HP 1651B



## HP 1650B, HP 1651B, HP 1654B: Best Value in General-Purpose Logic Analyzers

For microprocessor analysis or general-purpose state and timingdebug, the HP 1650B, HP 1651B, and HP 1654B logic analyzers offer the best value. Each analyzer can be configured as a one- or two-state analyzer, a state analyzer/timing analyzer, or a timing analyzer. Data captured by each analyzer can be displayed with full time correlation. Lightweight, flexible, passive probing is included.

### Support for Most Microprocessors, Buses, and Interfaces

The HP 1650B, 1651B and 1654B support a broad range of microprossessors, buses, and interfaces. Each support package turns your analyzer into a powerful debugging tool dedicated to the task at hand. Most support packages include a 31/2-inch disk that configures the analyzer and translates captured data into mnemonics. See pages 324-326 for details on support for your system.

Label >	ADDR	DATA	Time		
Bose >	Hex	Hex	Rel		
-0001	0004F6	8930	1.24 us		
x +0000	008930	B03C	1.24 us		
+0001	008932	OOFF	1.24 us		
+0002	008934	67F8	1.28 us		
+0003	008936	B03C	1.24 us		
+0004	00892E	61FA	1.72 us		
+0005	008930	BO3C	1.28 us		
Time/Div	500 ns	Delay	0 s	O to Trigger	2.240 us
	000	, , , ,		منمند	بكتك
LOCK 00 S 00 DS 00 DS 00		=			$\equiv \equiv$

Time Correlated State and Timing Displayed on the Same Screen

### Powerful State Analysis Helps You Focus on Needed Information

The HP 1650B's powerful state triggering filters out unnecessary data and provides a listing of the crucial data:

- Clocks and clock qualifiers allow your system to determine when the analyzer takes a sample.
- Storage qualification allows you to specify which states are stored in memory.
- Eight sequence levels determine the sequence of states required for trigger and help you to focus on a specific area of code execution.
- Eight pattern recognizers, one range recognizer, or logical combinations of these are used to identify stored states.
- Tagging keeps track of the amount of time or the number of states between stored states.
- · Enable/disable can be used to restrict storage to the activity of a specific routine.

#### Transitional Timing on All Channels Extends the Measurement Range

Each analyzer provides 10 ns timing resolution on every channel. The analyzer stores data only when there is a transition, thus avoiding redundant data storage. 100 MHz transitional timing on all channels effectively extends the memory by lengthening the time covered by the acquisition. Because timing analyzer samples at full speed, events that are seconds or minutes apart are captured with 10 ns resolution. You can use pattern, edge, or duration triggering across all 80, 64, or 32 channels when you need to see what is happening around a hardware interrupt or handshake.

## Glitch Capture on All Channels

With glitch capture on all channels, you no longer need to move probes around your system to detect intermittent problems. You can trigger on and capture 5 ns glitches on all channels of your HP 1650B, 1651B, and 1654B analyzers. Glitches are displayed as vertical dashed lines, so you can easily distinguish legitimate system activity from glitches.

# 328

## LOGIC ANALYZERS

## Portable Logic Analyzers (cont'd) HP 1650B, 1651B, 1654B

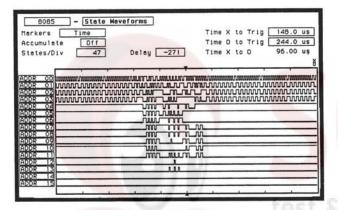
## Capture the Data You Want to See

You can trigger on a pattern across the full number of channels, or qualify a pattern by specifying a duration, glitch, or edge. Or you can specify a pattern duration to capture error conditions indicated by a pattern that exceeds a specified limit. When you need to see what is happening around a troublesome glitch or hardware interrupt signal, use glitch or edge triggering. Use postprocessing to determine statistical variance of edge placement, or to detect propagation delays that fall outside specified values.

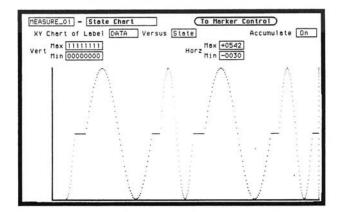
## Flexible Data Display Modes Decrease Debugging Time

Display state acquisitions in one of five forms:

- State listing, which displays your acquisition as a binary, hexadecimal, octal, decimal, or ASCII listing
- Symbolic/inverse assembler, which displays your data in easyto-interpret mnemonics
- State waveform mode, which displays the data in waveform diagram format
- X-Y chart mode, which displays the value of a data label versus acquisitions or other labels
- State compare mode, which enables you to compare an acquisition to previously acquired state data



View an entire state acquisition at a glance to examine bus activity or processor control, fully correlated with state listing, X-Y chart, and compare modes.



Plot label versus states to check A/D converters or memory coverage. Plot label versus label to obtain a circuit or routine "signature." Correlate chart display with inverse assembled listing.

HEHSUKE		1 - State		
Show Di	11	erence Lis	ting	Find Difference 0 (Full Compore
Mask	>	•••••	•	
Label	>	DATA	STROBE	1
Bose	>	Bin	Hex	]
+0006		00010111	1	
+0007		00011110	1	
+0008		00100101	1	
+0009		00101110	1	
+0010		00110111	1	
+0011		01000000	1	
+0012		01001010	1	
+0013		01010100	1	
+0014		01011111	1	
+0015		01101010	1	
+0016		01110101	1	
+0017		10000000	1	
+0018		10000000	1	
+0019		10000000	1	
+0020		10000000	1	

Compare state acquisitions with previously acquired state data. Select channel and range masks to "zoom in" on important data. Repetitively capture data until compare is equal or not equal.

## Lightweight, Flexible Probing

Like all HP logic analyzers, the HP 1650B, 1651B, and 1654B feature lightweight, flexible, passive probing. Passive probing means lower cost and increased reliability, because no active circuitry is needed at the probe tip. Measurement quality is not sacrificed; each probe only loads your digital system with 100 K $\Omega$  and 8 pF.

## Compact and Portable

With their small footprint, the HP 1650B, 1651B, and 1654B fit easily on your workbench, within your field of vision along with the device you're working on. At only 22 pounds, these analyzers can be carried easily with the built-in handle or soft carrying case. The case allows you to keep all the probes and cables conveniently stored on top of the instrument.

## **Measurement Compatibility**

Save setup time by transferring state and timing configurations or measurements made with one instrument to another. Make measurements in the field with confidence that the setups and data can be reproduced later in the lab. Use a 3½-inch disk to transfer data from one analyzer to another. Use the HP 10392A state-to-pattern generator link to transfer activity captured in the field to the HP 16500A digital pattern generator to duplicate failure modes in the lab.

#### Programmability Over HP-IB or RS-232

Both HP-IB and RS-232 interface ports are standard equipment on the HP 1650B, HP 1651B, and 1654B. You can program front-panel functions from either interface, or send hardcopy output to HP-IB or RS-232 printers.

## **Built-in Upgrade Path**

The HP 1650B/1651B feature a built-in upgrade path to the HP1652B/1653B logic analyzers with built-in oscilloscopes. You can get a 32- or 80-channel analyzer now and upgrade with a 400 MSa/s oscilloscope when you need to.

# The HP 1651B and 1654B – for 8- and 16-Bit Microprocessor Applications

The HP 1651B and 1654B offer all the features of the HP 1650B, except channel count (and state speed on the HP 1651B). For debugging most 8-bit processors, or to monitor timing activity across up to 32 channels, the HP 1651B is the ideal tool. For debugging 16-bit processors, or to monitor 8-bit processors with extra channels for timing analysis, the HP 1654B is the perfect match.

#### Key Specifications and Characteristics

Model Number	HP 1650B	HP 1651B	HP 1654B  100 MHz all 64 channels  35 MHz all 64 channels  1 Kbit/channel	
Timing	100 MHz all 80 channels	100 MHz all 32 channels		
State	35 MHz all 80 channels	35 MHz all 32 channels		
Memory	1 Kbit/channel	1 Kbit/channel		
Microprocessor Support	Most 8-, 16-, and 32-bit microprocessors	Most 8-bit microprocessors	Most 8-, 16-bit microprocessors	

See page 343 for ordering information.